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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re application of:

Timothy J. VAN HOOK et al.

Appl. No.: 09/662,832

Filed: September 15, 2000

For: Alignment and Ordering of Vector

Elements for Single Instruction
Multiple Data Processing

Confirmation No. 2552

Art Unit: 2552 2/83

Examiner: Pan, D.

Atty. Docket: 1778.0100002

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Letter Re-Submitting Non-U.S. Patent Documents

Commissioner for Patents Washington, D.C. 20231

HANDCARRY TO:
Mr. Daniel Pan, CP 2, 2A05
4th Floor Reception

Sir:

In accordance with a request from Examiner Daniel Pan, on August 28, 2002, Applicants hereby re-submit copies of the non-U.S. Patent documents previously filed with an Information Disclosure Statement on May 22, 2002.

Applicants also hereby re-submit a copy of the form PTO-1449 listing all of the documents filed on May 22, 2002. Per a telephone conversation and agreement with Examiner Pan on September 5, 2002, the cited U.S. Patent documents are not being re-submitted. The re-submitted documents only include copies of the documents identified on the PTO-1449 with document reference numbers AJ1-AR1, AN2-AR2, AN3-AR3, AN4-AR4, AN5-AR5, AN6-AR6, AN7-AR7 and AN8-AR8.

Applicants note that the copy of the document No. AO4 submitted herewith is missing pages A-122 and A-172.

Timothy J. VAN HOOK et al. Appl. No. 09/662,832

- 2 -

It is respectfully requested that the attached postcard be stamped with the date of resubmission of the above listed documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Michael B. Ray

Attorney for Applicants Registration No. 33,997

Date:

1100 New York Avenue, N.W. Washington, D.C. 20005-3934 (202) 371-2600

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DOLLARS 15-122 540 Box: RCE Alignment and Ordering of Vector Elements for Single Instruction Multiple Data Atty: MBR/BAM/mjg **Docket:** 1778.0100002 THE SUM OF THE SUM OF THE When receipt stamp is placed hereon, the USPTO acknowledges receipt of the following documents: Examiner: Pan, D. Art Unit: 2183 Due Date: Please Date Stamp And Return To Our Courier 1778.0100002 Request for Continued Examination (RCE) Transmittal (PTO/SB/30); Our Check No. 35238 for \$ 740.00 to cover RCE application filing. A list of the cited documents on Form PTO-1449 (8 pages); A copy of the (88) documents cited on Form PTO-1449; STERNE, KESSLER, GOLDSTEIN & FOX PLLC Preliminary Amendment Under 37 C.F.R. § 1.114 FIRST UNION BANK • WASHINGTON, D.C. 20005 COMM. OF PATENTS & TRADEMARKS 1100 NEW YORK AVE. NW, SUITE 600 WASHINGTON, D.C. 20005 PTO Fee Transmittal Form (PTO/SB/17); September 15, 2000 Applicant: VAN HOOK et al. GOVERNMENT FEES ACCT. **ATTORNEYS AT LAW** Information Disclosure Statement; TO THE ORDER O Processing 09/662,832 Return postcard; and SKGF Cover Letter Filed: Application No.: For: SKGF_DC1:8663.1 PAY

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	AC1	4,02	3,023	05/19)77	Bourrez et al.			35	152			
	AD1	4,10	9,310 \	08/19	78	Eng	and <i>et al</i> .	3	64	200			
	AE1	4,12	8,880	12/19	78	Cray	/, Jr	3	64	200			
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	AG1	4,21	9,874	08/19	980	Gus	ev et al.	3	64	200			
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	AO	1	Bier, J., "DSF (September 1			mmur	nications Apps," <i>Mic</i>	roprocessor Re	eport, Mic	croDesign Re	sources, pp. 11-15		
	AP	1					SuperSPARC vs. 88 ober 4, 1991).	110," <i>Micropr</i> o	cessor R	eport, MicroD	esign Resources,		
	AQ	1	Diefendorff, K., "88410 Second Level Cache," Microprocessor Forum, Motorola Inc., 20 pages (November 1991).										
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	AD2	4,58	33,199	04/19	986	Boot	hroyd <i>et al</i> .	36	4	900			
	AE2	4,77	73,006	09/19	988	Kino	shita et al.	36	4	200			
	AF2	4,81	4,976	03/19	989	Hans	sen <i>et al.</i>	36	4	200			
	AG2	5,15	50,290	\ 09/19	992	Hunt		36	4	736			
	AH2	5,41	8,915	05/19	995	Matu	ıda et al	39	5	325			
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	AN	2	Fiegel, C.P., 25 (March 28	"TI Inti 3, 1994	roduces Fou	ır-Pipo	cessor DSP Chip," Micro	oprocessor f	Report, I	MicroDesign	Resources, pp. 22		
	AO	2	Gwennap, L. 10, No. 15, p				imedia Extensions," <i>Mi</i> 96).	croprocesso	r Repor	t, MicroDesig	n Resources, Vol.		
	AP	2	Gwennap, L. 10, No. 13, 1				to Enter the Fray," Mic	roprocessor	Report,	MicroDesigr	n Resources, Vol.		
	AQ	2	Hansen, C., "Architecture of a Broadband Mediaprocessor," COMPCON 96, IEE, pp. 1-8 (February 25-29, 1996).										
•	AR	<u>2</u>	DSP56002 2- L07277-L076	4-Bit D 62).	Digital Signal	Proce	ssor User's Manual, M	otorola, Inc.,	386 pa	ges (1995).	(Bates Numbers		
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	AA3	5,581,773	12/1996		Glove	er	3	95	800	
	AB3	5,590,345	12/1996		Barke	er et al.	3	95	800	
	AC3	5,666,298	09/1997		Peleg	et al.	3	64	715.08	
	AD3	5,669,010	09/1997		Duluk	c, Jr.	3	95	800.22	
	AE3	5,721,892	02/1998			et al.	3	95	562	
	AF3	5,734,874	03/1998			look et al.	3	95	513	
	AG3	5,740,340	04/1998		Purce	ell et al.	3	95	118	
	AH3	5,752,071	05/1998	T		s et al.		95	800.34	
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	AP	3 Turley, J. a	ind Hakkarain in Resources,	en, H., " , pp. 14-	'TI's N 17 (Fe	lew 'C6x DSP Screams ebruary 17, 1997).	s at 1,600 M	11PS," <i>M</i>	icroprocesso	r Report,
	AQ	<u>DSP56002</u> (Bates Nur	<i>24-Bit Digital</i> nbers L07803	Signal I -L07912	Proces 2).	ssor Semiconductor Te	echnical Da	ta, Motor	ola, Inc., 110	pages (1996).
-	AR	3 A236 Para pages (199	llel Digital Sigi 14). (Bates Nu	<i>nal Proc</i> umbers l	essor L1118	Chip Programmer's R 4-L11378).	eference M	anual, Q	xford Micro D	levices, Inc., 195
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	AA4		1,523 \	06/19	998	Wilki	nson et al.	395	800.2					
	AB4		8,241	07/19	998	Bind	loss et al.	395	800.2					
	AC4		4,602	07/19	998	Glas	s et al.	395	561					
	AD4		3,661	08/19			ng <i>et al.</i>	364	750.5					
	AE4		2,147	09/19	998		Hook <i>et al.</i>	345	511					
	AF4		2,723	09/19		Ohts	u <i>et al</i> .	385	128					
	AG4		5,723	09/19		+	nson et al.	395	800.2					
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	AO	4	DSP56000 2- L08722-L093	4-Bit D 59).	Digital Signal	l Proce	ssor Family Manual, Mot	orola, Inc., 638 p	pages (1995).	(Bates Numbers				
	AP	4	Microprocess	or De	vices Reliab	oility Mo	onitor Report: Third Quar	ter 1997, Motoro	la, 8 pages (19	997).				
	AQ	AQ 4 Reliability Monitor Environmental Stress Data 3Q97, Motorola, 4 pages (1997).												
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ATTY. DOCKET NO. APPLICATION NO. 1778.0100002 (0055.20US) 09/662,832 FORM RTO-1449 APPLICANT Van Hook et al. INFORMATION DISCLOSURE STATEMENT GROUP FILING DATE September 15, 2000 2183 **U.S. PATENT DOCUMENTS EXAMINER** DATE NAME **CLASS** SUB-FILING DATE **DOCUMENT** INITIAL **CLASS** NUMBER 800.22 395 AA5 5,864,703 01/1999 van Hook et al. 800.23 Park et al. 395 AB5 5,881,307 03/1999 Fischer et al. 364 754.03 AC5 5,936,872 08/1999 371 09/1999 Spracklen 53 AD5 5,960,012 712 AE5 5,996,056 11/1999 Volkonsky 1 12/1999 712 22 Dinkjian AF5 6,006,316 05/2000 712 7 Nguyen AG5 6,058,465 07/2000 Morton 712 22 AH5 6,088,783 AI5 FOREIGN PATENT DOCUMENTS **EXAMINER** INITIAL DOCUMENT NUMBER DATÊ COUNTRY CLASS SUB-TRANSLATION **CLASS** AJ5 Yes Nο Yes AK5 No AL₅ Yes No Yes AM5 No OTHER (Including Author, Title, Date, Pertinent Pages, etc.) ΑN <u>5</u> Microprocessor Devices Reliability Monitor Report: Fourth Quarter 1997, Motorola, 10 pages (1997). AO Reliability and Quality Report 4Q97, Motorola, 2 pages (1997). <u>5</u> "Hardware Implications of xmem as a st followed by a Id," Motorola Semiconductor Engineering Bulletin, ΑP <u>5</u> Motorola, 5 pages (September 1992). 601 First Silicon, at http://www.mot.com/SPS/PowerPC/library/press_releases/601_First_Silicon.html, Motorola, 2 AQ <u>5</u> Pages (October 1, 1992). DSP56002/DSP56L002 24-Bit Digital Signal Processor Motorola Semiconductor Product Information, Motorola, AR 5 Inc., 3 pages (1994). (Bates Numbers L07913-L07915). **EXAMINER** DATE CONSIDERED **EXAMINER**: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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	AP	<u>6</u>	TMS320C80	Digitai	l Signal Proc	essor	, Texas Instruments	orp., p. 41 (.	July 1994).					
	AQ	<u>6</u>	EB162: Progr (January 24,	62: Programming Tips (MC88110), at http://www.mot-sps.com/lit/html/eb162.html, Motorola, Inc., 1 page huary 24, 1997).											
	AR	<u>6</u>	MC88110/410 sps.com/lit/ht	DH/A ml/mc	<i>D: MC88110</i> 88110410dha)/MC8 ad.htr	88410 Designer's Handa nl, Motorola, Inc., 1 pag	book, at http ge (January	o://www.n 24, 1997	not- ').					
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APPLICATION NO. 09/662,832

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	AP	7	<i>TMS320C1x</i> , 1995). (Bate	TMS320C1x/C2x/C2xx/C5x Assembly Language Tools User's Guide, Texas Instruments, 483 pages (March 1995). (Bates Numbers L07916-L08398).										
	AQ	7 TMS320C5x General-Purpose Applications User's Guide, Texas Instruments, 167 pages (July 1997). (Bates Numbers L08399-L08565).												
•	AR	<u>7</u>	El-Sharkawy Prentice Hall	Moha PTR, _I	med, Ph.D., pp. 43-124 (. <i>Digita</i> 1996).	l Signal Processing App (Bates Numbers L065	plicatio 19-L06	ons with Moto 6601).	rola's DSP56	002 Processor,			
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ATTY. DOCKET NO. APPLICATION NO. 1778.0100002 (0055.20US) 09/662,832 FORM PTO-1449 **APPLICANT** Van Hook et al. INFORMATION DISCLOSURE STATEMENT **GROUP** FILING DATE 2183 September 15, 2000 **U.S. PATENT DOCUMENTS EXAMINER** DOCUMENT NUMBER INITIAL DATE NAME CLASS SUB-**FILING DATE CLASS** AA8 AB8 AC8 AD8 AE8 AF8 AG8 AH8 AI8 FOREIGN PATENT DOCUMENTS **EXAMINER** INITIAL DOCUMENT NUMBER DATE COUNTRY CLASS SUB-TRANSLATION **CLASS** AJ8 Yes No AK8 Yes No AL8 Yes No AM8 Yes No OTHER (Including Author, Title, Date, Pertinent Pages, etc.) AN Killian, E., "MIPS Extensions for Digital Media," Microprocessor Forum, pp. 1-5 (October 22-23, 1996). 8 "Interrupt Latency in the MC88110," Motorola Semiconductor Engineering Bulletin, Motorola, pp. 1, 2 and 4-9 AO <u>8</u> (1992).AP <u>8</u> "Running the MC88110 in Lockstep," *Motorol*ą *Semiconductor Engineering Bulletin*, Motorola, 2 pages (1992). Turley, J., "Siemens TriCore Revives CISC Techniques: New 32-Bit Design Emphasizes DSP Capability and AQ 8 Microcontroller Functions," Microprocessor Report, Microdesign Resources, pp. 13-16 (November 17, 1997). i860™ Microprocessor Family Programmer's Reference Manual, Intel Corporation, 79 pages (1992). (Bates AR <u>8</u> Numbers L09361-L09439). **EXAMINER** DATE CONSIDERED

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conformance and not considered. Include copy of this form with next communication to Applicant.

Page 1 of 8 ATTY. DOCKET NO. APPLICATION NO. 1778.0100002 (0055.20US) 09/662,832 FORM RTO-1449 APPLICANT Van Hook et al. INFORMATION DISCLOSURE STATEMENT FILING DATE GROUP 2183 September 15, 2000 **U.S. PATENT DOCUMENTS EXAMINER** INITIAL DOCUMENT DATE NAME **CLASS** SUB-FILING DATE NUMBER CLASS AA1 3,654,621 04/1972 Bock et al. 340 172.5 AB1 3,916,388 10/1975 Shimp et al. 340 172.5 AC1 05/1977 4,023,023 Bourrez et al. 235 152 08/1978 AD1 4,109,310 England et al. 364 200 AE1 12,1978 4,128,880 Cray, Jr. 364 200 AF1 12/1978 4,130,880 Nutter 364 900 08/1980 AG1 4,219,874 Gusev et al. 364 200 AH1 02/198 4,317,170 Wada et al. 364 200 AI1 4,396,982 08/1983 Wada et al. 364 200 FOREIGN PATENT DOCUMENTS **EXAMINER** INITIAL DOCUMENT NUMBER DATE COUNTRY CLASS SUB-TRANSLATION CLASS AJ1 PCT WO 97/07450 A1 02/1997 G06F 7/00 N/A AK1 WO 97/08608 A1 03/1997 PCT G06F 7/00 N/A AL₁ 0 681 236 A1 11/1995 EP G06F 9/302 N/A AM₁ 0 681 236 B1 11/2000 EP G06F 9/302 N/A OTHER (Including Author, Title, Date, Pertinent Pages, etc.) An Introduction to Thumb™, Version 2.0, Adyanced RISC Machines, 29 pages (March 1995). AN 1 Bier, J., "DSP16xxx Targets Communications Apps," Microprocessor Report, MicroDesign Resources, pp. 11-15 AO 1 (September 15, 1997). Case, B., "Superscalar Techniques: SuperSPARC vs. 88110," Microprocessor Report, MicroDesign Resources, AP 1 Vol. 5, No. 22, pp. 1 and 6-11 (December 4, 1991). Diefendorff, K., "88410 Second Level Cache," Microprocesson, Forum, Motorola Inc., 20 pages (November ΑQ 1 1991). Diefendorff, K. and Allend, M., "Organization of the Motorola 88110: A Superscalar RISC Microprocessor," 12 AR 1 pages, Source and date unknown. **EXAMINER** DATE CONSIDERED **EXAMINER**: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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ATTY. DOCKET NO. 1778.0100002 (0055.20US)

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	AA2	4,491,910	Q1/1985		Caudel et al.	364	200	
	AB2	4,507,731	03/1985		Morrison	364	200	
	AC2	4,520,439	05/1,985		Liepa	364	200	
	AD2	4,583,199	04/1986		Boothroyd et al.	364	900	
	AE2	4,773,006	09/1988		Kinoshita et al.	364	200	
	AF2	4,814,976	03/1989		Hansen et al.	364	200	
	AG2	5,150,290	09/1992		Hunt	364	736	
	AH2	5,418,915	05/1995		Matuda <i>et al</i> .	395	325	
	Al2	5,513,366	04/1996	$\overline{}$	Agarwal <i>et al</i> .	395	800	
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	AL2							Ye N
	AM2				\			Ye
	AO	Gwennap, L	:h 28, 1994). 	IPS Add	Processor DSP Chip," <i>Mic</i> Multimedia Extensions" <i>M</i> 3, 1996).			
	AP	2 Gwennap, L 10, No. 13,	., "New Multi 1 page (Octol	media Cl ber 1996	hips to Enter the Fray," <i>Mid</i>).	croprocessor Report,	MicroDesign	Resources, Vol.
	AQ	2 Hansen, C., 1996).	"Architecture	e of a Bro	padband Mediaprocessor,"	COMPCON 96, IEE,	pp. 1-8 (Febr	uary 25-29,
	AR	2 DSP56002 2 L07277-L07	24-Bit Digital 662).	Signal Pi	rocessor User's Manual, N	lotorola, Inc., 386 pag	jes (1995). (E	Bates Numbers
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	AA3	5,5	81,773	12/1	996	Glov	er		395	800	
	AB3	5,5	90,345	12/1	996	Bark	er <i>et al.</i>		395	800	
	AC3	5,6	66,298	09/1	997	Pele	g et al.		364	715.08	
	AD3	5,6	69,010	09/1	997	Dulu	k, Jr.		395	800.22	
	AE3	5,72	21,892	02/1	998	Pele	g <i>et al.</i>		395	562	
	AF3	5,73	34,874	03/1	998	Van	Hook <i>et al</i> .		395	513	
	AG3	5,74	40,340	04/1	998	Purc	ell et al.		395	118	
	AH3	5,7	52,071	05/1	998	Tubl	s et al.		395	800.34	
	AI3	5,7	58,176	05/1	998	wal <i>et al</i> .		395	800		
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	AN	3	(August 1996	·).			nology Extension to the				
	AO	3	Inc., 7 pages	(Janu	ary 1996).	-,					as monuments,
	АР	<u>3</u>	Turley, J. and MicroDesign	l Hakk Resou	arainen, H., irces, pp. 14	"TI's N -17 (F	lew 'C6x DSP Screams ebruary 17, 1997).	at 1,6	00 MIPS," <i>Mi</i> d	croprocessor	Report,
AQ 3 DSP56002 24-Bit Digital Signal Processor Semiconductor Technical Data, Motorola, Inc., 110 page (Bates Numbers L07803-L07912).											pages (1996).
-	AR	3	A236 Parallel pages (1994)	Digita	al Signal Proc es Numbers	cessor L1118	Chip Programmer's Re 4-L11378).	ferenc	e Manual, Ox	ford Micro De	evices, Inc., 195
EXAMINER					· · · · · · · · · · · · · · · · · · ·				DATE CONS	SIDERED	-

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	AA4	5,7	61,523	06/1	998	Wilk	inson <i>et al</i> .		395	800.2		
	AB4	5,7	78,241	07/1	998	Bind	loss <i>et al</i> .		395	800.2		
···	AC4	5,7	84,602	07/1	998	Glas	s et al.		395	561		
	AD4	5,7	93,661	08/1	998	Dulo	ng et al.	3	364	750.5		
	AE4	5,8	12,147	09/1	998	Van Hook <i>et al.</i>		3	345	511		
	AF4	5,8	12,723	09/1	1998 Ohtsu		su <i>et al.</i>	3	385	128		
	AG4	5,8	15,723	09/1	998	Wilkinson et al.		3	395	800.2		
	AH4	5,8	22,606	10/1	10/1998		on	3	395	800.16		
	Al4	5,8	38,984	11/1	998	Ngu	yen <i>et al.</i>	3	395	800.05		
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	AJ4										Yes	
	AK4										Yes No	
	AL4										Yes No	
	AM4										Yes No	
	AN	4	Lee, R., "Aco	elerati	ng Multimed	dia with	itle, Date, Pertinent Pa			IEEE, pp. 22	-32 (April 1995).	
	AO	4	(Bates Numbers L08566-L08576).									
	AP	<u>4</u>	Microprocess	sor De	vices Reliab	ility Mo	nitor Report: Third Quai	rter 1997, i	Motorola,	8 pages (19	97).	
	AQ	<u>4</u>	Reliability Mo	nitor E	Environment	al Stres	ss Data 3Q97, Motorola,	, 4 pages (1997).			
-	AR	4	Reliability and	d Qual	ity Report 30	Q97, M	otorola, 2 pages (1997)			,,,		
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	AA5	5,8	64,703	01/1	999	van	Hook <i>et al</i> .	395	800.22	
	AB5	5,8	81,307	03/1	999	Park	et al.	395	800.23	
	AC5	5,9	36,872	08/1	999	Fisc	ner <i>et al</i> .	364	754.03	
	AD5	5,9	60,012	09/1	999	Spra	cklen	371	53	
	AE5	5,9	96,056	11/1	999	Volk	onsky	712	1	
	AF5		06,316	12/1		Dink	jian	712	22	
	AG5		58,465	05/2		Ngu	/en	712	7	
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	AN	<u>5</u>	Microprocess	or De	vices Reliabi	ility Mc	onitor Report: Fourth Quarte	<i>r 1997</i> , Motorol	a, 10 pages (1997).
	AO	<u>5</u>	Reliability and	d Qual	ity Report 40	⊋97, N	lotorola, 2 pages (1997).			
	AP	<u>5</u>	"Hardware Im Motorola, 5 p	iplicati ages (ons of <i>xmen</i> September 1	7 as a 1992).	st followed by a ld," Motorol	a Semiconducto	or Engineerin	g Bulletin,
	AQ	<u>5</u>	601 First Silic 2 Pages (Oct	on, at ober 1	http:/www.m , 1992).	ot.com	n/SPS/PowerPC/library/pres	ss_releases/601	_First_Silico	n.html, Motorola,
-	AR	<u>5</u>	DSP56002/D. Inc., 3 pages	S <i>P56L</i> (1994)	<i>002 24-Bit D</i>). (Bates Nu	Digital :	Signal Processor Motorola S L07913-L07915).	Semiconductor I	Product Inform	mation, Motorola,
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	AN	<u>6</u>	MC88410UM sps.com/lit/ht	/AD: M ml/mc	1C88410 Sec 88410umad.	conda. html, l	ry Cache Controller Use Motorola, Inc., 1 page (J	er's Manu January :	<i>ial, at</i> http:/ 24, 1997).	/www.mot-	
	AO	<u>6</u>	AN1214: MCt sps.com/lit/ht	38110 ml/an1	<i>64-bit Extern</i> I214.html, M e	nal Bu otorol	s Interface to 16-bit EPF a, Inc., 1 page (January	ROM, at 24, 199	http://www. 7).	mot-	
	АР	6	TMS320C80	Digital	Signal Proce	essor,	Texas Instruments Corp	p., p. 41	(July 1994).	
	AQ	<u>6</u>	EB162: Progr (January 24,	ammir 1997).	ng Tips (MC8	38110)), at http://www.mot-sps.	.com/lit/h	ntml/eb162.	html, Motorol	la, Inc., 1 page
•	AR	6	MC88110/410 sps.com/lit/htt	DH/A	<i>D: M</i> C88110, B8110410dha	/MC8/ ad.htn	8410 Designer's Handbo nl, Motorola, Inc., 1 page	ook, at h	ltp://www.m ry 24, 1997	iot-).	
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September 15, 2000 2183 **U.S. PATENT DOCUMENTS EXAMINER** INITIAL **DOCUMENT** DATE NAME **CLASS** SUB-FILING DATE NUMBER CLASS AA7 AB7 AC7 AD7 AE7 AF7 AG7 AH7 AI7 **FOREIGN PATENT DOCUMENTS EXAMINER** INITIAL **DOCUMENT NUMBER** DATE COUNTRY CLASS SUB-TRANSLATION CLASS AJ7 Yes No AK7 Yes No AL7 Yes AM7 Yes No OTHER (Including Author, Title, Date, Pertinent Pages, etc.) MC88110UMAD: MC88110 Second Generation RISC Microprocessor User's Manual, at http://www.mot-ΑN <u>7</u> sps.com/lit/html/mc88110umad.html, Motorola, Inc., 1 page (January 24, 1997). Weiss, R., ARM Piccolo Mixes ARM RISC with DSP, at http://www.estd.com/Editorial/1996/11/Briefs/arm.html, 1 AO 7 page, (November 1996). TMS320C1x/C2x/C2xx/C5x Assembly Language Tools User's Guide, Texas Instruments, 483 pages (March AP 7 1995). (Bates Numbers L07916-L08398). TMS320C5x General-Purpose Applications User's Guide, Texas Instruments, 167 pages (July 1997). AQ 7 (Bates Numbers L08399-L08565). El-Sharkawy, Mohamed, Ph.D., Digital Signal Processing Applications with Motorola's DSP56002 Processor, AR Z Prentice Hall PTR, pp. 43-124 (1996). (Bates Numbers L06519-L06601). **EXAMINER** DATE CONSIDERED

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	AO	8	"Interrupt Latency in the MC88110," <i>Motorola Semiconductor Engineering Bulletin</i> , Motorola, pp. 1, 2 and 4-9 (1992).								
	4.5										
	AP	8	"Running the MC88110 in Lockstep," Motorola Semiconductor Engineering Bulletin, Motorola, 2 pages (1992).								
	AQ 8 Turley, J., "Siemens TriCore Revives CISC Techniques: New 32-Bit Design Emphasizes DSP Capability of Microcontroller Functions," <i>Microprocessor Report</i> , Microdesign Resources, pp. 13-16 (November 17, 19)									Capability and ober 17, 1997).	
,	AR	<u>8</u>	8 i860™ Microprocessor Family Programmer's Reference Manual, Intel Corporation, 79 pages (1992). (Bates Numbers L09361-L09439).								
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